







GCET AICTE IDEA Lab

Let's nurture the product of your innovative idea.

Activity Newsletter: 2021 September-October Issue-02

G H Patel College of Engineering & Technology (A Constituent College of CVM University)

OUTLINE

Message From Editors	1
AICTE IDEA LAB Awareness Program Photoshop Workshop	2
Chief Mentors Conclave	14 15

Message From Editors





Well begun is half done- Aristotle. This phrase fits right in showcasing the team efforts made by GCET AICTE IDEA Lab Team members. We are much delighted to bring to you this second issue of our

bi monthly newsletter. September is a month of welcoming new students on-campus, who are super excited to start their journey of Engineering. With the onset of First year Engineering studies, Faculties and student team members of IDEA Lab conducted orientation program. Under this orientation, students were briefed with the concept of IDEA Lab and also guided them to effectively utilize this lab facility. The hands-on exercise of photography and VFX tools was conducted by the IT Department for all the interested students. With this we wish that, as the IDEA Lab has started accelerating, the journey keeps its pace and students will get the utmost benefit of the same.

Prof. Aakarsh S Jain & Prof. Endrick D Contractor Editors,
GCET AICTE IDEA Lab Activity Newsletter

Department of Computer Engineering & Computer Science and Design

The Department of Computer Engineering and Computer Science and Design had organized a Talk on "AICTE IDEA LAB Scheme Introduction", which was delivered by Dr. Vijay Makwana, the faculty coordinator. The event was organized on 13th October, 2021 from 10:30 am to 11:30 am. The Talk was scheduled for the First Year Students out of which 45 students of Computer Engineering Branch and 25 students of Computer Science and Design attended the event Dr. Vijay Makwana delivered a general overview of the AICTE IDEA LAB Scheme and a brief introduction to IOT.

The other topics covered in the session were: Brief Information of IDEA LAB, Objective of AICTE IDEA LAB Scheme, Infrastructure facilities provided in GCET AICTE IDEA LAB, Events & Activities under AICTE IDEA LAB Scheme, Expected Outcomes of GCET AICTE IDEA LAB and Utilization of GCET AICTE IDEA LAB facilities for miscellaneous projects. The main objective of the event was to give students an insight of what GCET AICTE IDEA LAB is and its benefits.

Department of Chemical Engineering

The Department of Chemical Engineering had organized a Talk on "AICTE IDEA Lab Scheme Introduction", which was delivered by Prof. Endrick D Contractor, the faculty coordinator, Chemical Engineering Department, GCET. The event was held on October 14th, 2021, from 10:30 a.m. to 11:30 a.m. The Talk was scheduled for the First Year Students out of which 23 students attended the event. Prof. Endrick D Contractor has provided general information of AICTE IDEA Lab Scheme and all the possible outcomes.



Department of Electronics & Communication Engineering

Department of Electronics & Communication Engineering had organized a Talk on "AICTE IDEA Lab Scheme Introduction", conducted by Dr. Deven Trivedi, faculty coordinator, Electronics & Communication Department, and two 3rd year Electronics & Communication students Mr. Himanshu Patil and Mr. Anubhav Srivastav. The event was held on October 14th, 2021, from 10:30 am to 11:30 am. The Talk was scheduled for the First Year Students out of which 11 Students attended the event. Dr. Deven Trivedi has provided general information of AICTE IDEA Lab Scheme and demonstrated two Videos on Touch Witch Water Level Indicator.

The other topics covered in the session were: Brief Information of IDEA Lab, Objective of IDEA Lab Scheme, Infrastructure facilities provided in IDEA Lab, Events & Activities under IDEA Lab Scheme, Expected Outcomes of IDEA Lab, Utilization of IDEA Lab facilities for miscellaneous projects and Touch Switch and Water Level Indicator Demonstration.





Department of Civil Engineering

The Department of Civil Engineering had organized a Talk on "AICTE IDEA Lab Scheme Introduction", which was delivered by Prof. Poorav Shah, the faculty coordinator, Civil Engineering Department, GCET. The event was organized on 11th October, 2021 from 10:30 a.m. to 11:30p.m. The Talk was scheduled for the First Year Students out of which 9 students attended the event. Prof. Poorav Shah has provided general information of AICTE IDEA Lab Scheme.

The other topics covered in the session were: Brief Information of IDEA Lab, Objective of IDEA Lab Scheme, Infrastructure facilities provided in IDEA Lab, Events & Activities under IDEA Lab Scheme, Expected Outcomes of IDEA Lab and Utilization of IDEA Lab facilities for miscellaneous projects.



Department of Electrical Engineering

Department of Electrical Engineering had organized a Talk on "AICTE IDEA Lab Scheme Introduction", conducted by Dr Vijay Makwana and Prof. Sachin Patel, faculty coordinators, Electrical Department, and two 3rd year Electrical students Mr Harshil Sathwara and Mr Mukul Goray. The event was held on October 5th, 2021, from 10:30 a.m. to 11:30 a.m. The Talk was scheduled for the First Year Students out of which 12 students attended the event.

The topics covered in the session were: Brief Information of IDEA Lab, Objective of IDEA Lab Scheme, Infrastructure facilities provided in IDEA Lab, Events & Activities under IDEA Lab Scheme, Expected Outcomes of IDEA Lab, Utilization of IDEA Lab facilities for miscellaneous projects and Binary Encoder Representation using 7 Segment LED. Dr Vijay Makwana has provided general information on AICTE IDEA Lab Scheme. Prof. Sachin Patel has conducted a hands-on session on the operation of Binary Encoder Representation using 7 Segment LED. Mr Harshil Sathwara and Mr Mukul Goray, students of the third-year Electrical Department have demonstrated the 7 segments of LED.



Department of Information Technology

Department of Information Technology had organized a Talk on "AICTE IDEA Lab Scheme Introduction", which was conducted by Prof. Jay Vala and Prof. Bhargesh Patel, faculty coordinators and Smit Makhecha and Krupang Buddhdev, student coordinators of IT department. The event was held on October 8th, 2021, 11:30 a.m. The Talk was scheduled for the First Year Students out of which 29 students attended the event. Prof. Jay Vala has provided general information of AICTE IDEA Lab Scheme. Mr. Smit Makhecha and Mr. Krupang Buddhdev, students of third year Information Technology Department had demonstrated the IOT project.



Department of Information Technology

Department of Information Technology had organized a Talk on "AICTE IDEA Lab Scheme Introduction", which was conducted by Prof. Jay Vala and Prof. Bhargesh Patel, faculty coordinators and Smit Makhecha and Krupang Buddhdev, student coordinators. The event was held on October 12th, 2021, 11:30 a.m. The Talk was scheduled for the First Year Students out of which 24 students attended the event. Prof. Jay Vala has provided general information of AICTE IDEA Lab Scheme.



Department of Mechatronics Engineering

The Department of Mechatronics Engineering had organized a Talk on "AICTE IDEA Lab Scheme Introduction", which was delivered by Prof. Bhavik A Ardeshana, the faculty coordinator, Mechatronics Engineering Department, GCET. The Talk was scheduled for the First Year Students. Prof. Bhavik A Ardeshana had provided general information of AICTE IDEA Lab Scheme and all the possible outcomes.

Department of Mechanical Engineering

Department of Mechanical Engineering had organized a Talk on "AICTE IDEA Lab Scheme Introduction". Students were introduced to the faculty coordinators of IDEA Lab viz. Prof. Aakarsh Jain, Prof. Jvalant Trivedi, Prof Purnank Bhatt, Prof Mihir Solanki and Dr. Mukesh Bulsara. There was a formal introduction given by the student coordinators Mr.Sarang Pandya, Mr.Tirth Joshi, Mr.Jay Shah, Mr.Deep Pathak, Mr.Mitesh Maru, Mr.Yash Ranparia, Mr.Kunalsinh Rana of 3rd year Mechanical Department. The event was held on September 29th, 2021, from 2:30p.m. 3:30 p.m. The Talk was scheduled for the First Year Students out of which 14 students attended the event.

The purpose behind the Session was to pass on the information to the newly admitted students about IDEA Lab so that they can explore the possibilities of activities, events, etc. under the Banner of IDEA-Lab. In line to this, a few glimpses that were shared with the participants are as follows: Brief Information of IDEA Lab, Objective of IDEA Lab Scheme, Infrastructure facilities provided in IDEA Lab, Events & Activities under IDEA Lab Scheme, Expected Outcomes of IDEA Lab, Utilization of IDEA Lab facilities for miscellaneous projects, Presentation of IDEA Labs around the country and Digital demonstration of Portable Solar Cooker and Puri Maker prepared by student coordinators. Student Coordinators interacted with new admitted students by introducing themselves and make them familiar with activities and benefits of Idea lab.

A Hands-On Activity was also conducted for the students in the session. The students prepared a Structure using newspaper and demonstrated the stability of it. This was a learning experience for the students about the importance of Design considerations in manufacturing of the structures. The activity was supervised by Prof Jvalant Trivedi. The session ended with a vote of thanks by the Speaker and the Student Coordinators.

Photoshop workshop

The Department of Information Technology under GCET Idea Lab organized a hands-on workshop on "Photoshop Session for Post Creation". This workshop was successfully conducted on 22nd September at 9:30 a.m. under the leadership of Prof. Jay Vala and Prof. Bhargesh Patel. The student coordinators of the event were Aayushee Gurjar and Yatri Shah. There were a total of 12 participants, including the IT committee from the Idea Lab.

Aayushee Gurjar and Yatri Shah conducted this workshop of Photo editing and Adobe Photoshop in class A-314. The first phase in this Workshop was to install and set up the application on everyone's personal computer. Aayushee Gurjar gave a brief overview of the application and showed all of the application's basic tools.





Then after, Yatri Shah gave the participants some tasks and demonstrated how to use the application, as well as what tools to use and when to use them. Students learned about colour and contrast, poster size, and when to use specific elements. She also displayed and discussed how she worked on one of her earlier photo editing and banner-making projects, observing which students could learn and expand their thinking abilities in order to improve their design skills.

Finally, they were assigned the task of creating a unique design and submitting it before the deadline to ensure their completion of the event.

VFX Event

The Department of Information Technology hosted an event on VFX for Video Animation. They conducted a hands-on session on 9th September, 2021 at 4:30 p.m. under the leadership of Prof. Jay Vala and Prof. Bhargesh Patel. The student coordinators of the IT department were Nanil Oza, Aayushee Gurjar, Yatri Shah and Smit Makhecha. There were a total of 12 participants, including the IT committee from the Idea Lab. The Department of Information Technology hosted an event on VFX for Video Animation. They conducted a hands-on session on 9th September, 2021 at 4:30 p.m. under the leadership of Prof. Jay Vala and Prof. Bhargesh Patel. The student coordinators of the IT department were Nanil Oza, Aayushee Gurjar, Yatri Shah and Smit Makhecha. There were a total of 12 participants, including the IT committee from the Idea Lab.



Chief Mentors Conclave

Dr. Kaushik Nath, Head of Chemical department attended AICTE Chief Mentors Conclave on 21-22 October 2021 at AKGEC, Ghaziabad and NSUT, Delhi. It was attended by IDEA Lab Chief Mentors and Management Representatives from 51 institutes. The trainers in all the IDEA Labs have gone through intensive 7-day hands-on training through Advanced FDPs organized in the areas of Smart Manufacturing by College of Engineering (Pune) and AK Garg Engineering College (Ghaziabad) and in Electronic Systems Design by Netaji Subhas University of Technology (Delhi) and DY Pal International University (Pune).

A vigorous discussion took place such as: Smart manufacturing processes use digital information to optimize product, factory, and supply-chain operations. Advances in sensing, communicating, and analyzing digital information have introduced a vast array of new opportunities for increasing efficiency, lowering costs, and improving quality. It was a quite interesting session.

After attending the above mentioned FDPS, all the attendees' discussed about the FDPS in front of IDEA Lab faculty team of G H Patel College of Engineering & Technology. They got a good exposure to the trending technologies in their respective fields.



Engineering Trivia

- According to the Global Energy Statistical Yearbook, global electricity production for 2015 approached 25,000 terawatt hours (1 terawatt = 1 trillion watts).
- A bolt of lightning can measure up to three million (3,000,000) volts (and lasts less than one second).
- Electricity travels at the speed of light, about 300,000 kilometers per second.
- A spark of static electricity can measure up to 3,000 volts. The average taser emits 50,000 volts.
- Electric fields work in a similar way to gravity. Whereas gravity always attracts, electric fields can either attract or repulse.
- Did you know that electricity plays a role in the way your heart beats? Electricity causes muscle cells in the heart to contract. Electrocardiogram (ECG) machines, used by medical professionals, measure the electricity going through the heart. As the heart beats in a healthy person, the ECG machine displays a line moving across the screen with regular spikes. going through the heart.

GCET AICTE IDEA Lab Departmental Students' Team

ME Department

EC Department

Chinmay Rudragoudar

Deep Pathak

Jay Shah

Kunal Rana

Mitesh Maru

Sarang Pandya

Tirth Joshi

Yash Ranpariya

Anubhav Shriwastava Himanshu Patil Mohsin Ali Richa Mishra Rishi Sha Shrushti Parikh

Vraj Patel

Vidisha Sodha

MC Department

Abhishek Borad

Achyut Sapariya

Anil Maity

Ishan Shah

Mit Patel

Lovish Ojha

Rohan Umraliya

Shivam Rana

Shubham Sharma

Smeet Shah

Vinit Trivedi

Mehul Kamani

CP Department

Abhishek Pandey

Alay Parikh

Bhumika Ailawadi

Darshan Modi

Dhruvi Vadariya

Dhyey Dodiya

Disha Patel

Ishan Bhatt

Jhanvi Shukla

Kinal Patel

Moinuddin Mulla

Nishi Patel

Nishith Soni

Prashant Solanki

Riya Patel

Sitanshu Goradia

Smit Choksi

Sneh Vachhani

Vraj Mistry

Yash Kotadia

IT Department

Aayushee Gurjar

Adnan Lokhandwala

Henali Shah

Henil Patel

Jinay Shah

Jyot Prajapati

Krish bhadvaniya

Krishshan Navadia

Nainil Oza

Priyan Nakrani

Rahi Patel

Raj Ami

Siddharth Chudasma

Smit Makhecha

Sujal Lakhani

Yash Patel

Yatri Shah

CH Department

CL Department

Aaditya Rai Aditi Thattil Dipti Rathod Jay Chauhan Kanaiya Patil Kushal Mehta Mahir Mansuri

Neer Patel
Nil Zalavadiya
Sagar Gohil
Sania Pillai
Harshil Barbhaya
Hemal Dhankecha

Aditya Lodhari Kishan Kacha Nisarg Patel Pankaj Solanki Ravirajsinh Vaghela Yash Shaparia

EE Department

Dhaval Kalathiya
Dhruv Bhargav
Dhyey Vithalani
Emmanuel Kamugisha
Gautam Prajapati
Harshil Sathwara

Hetvi Shah

Mukul Goray

Nand Patel

Parth Vaghela

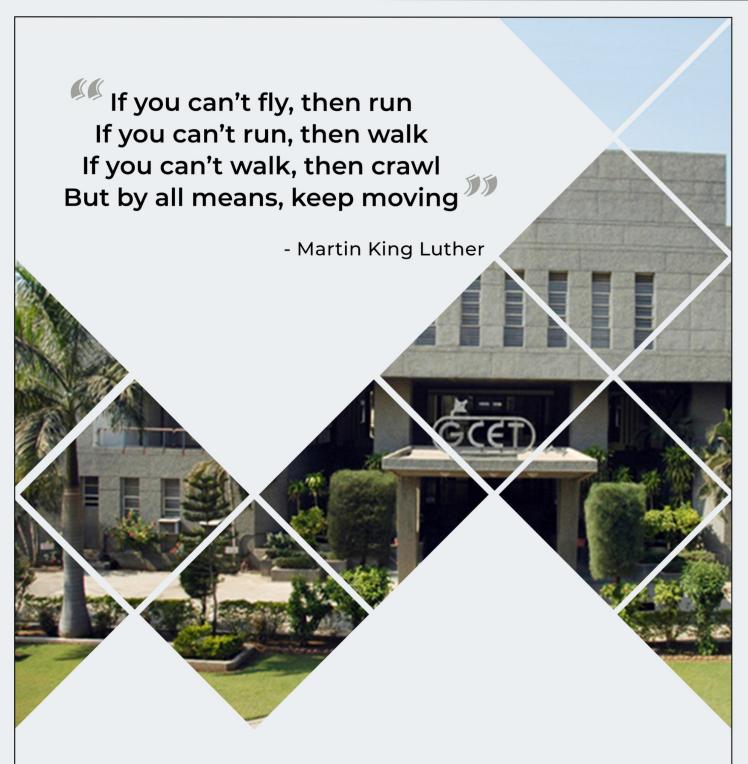
Shivam Patel

Helly Parikh

Links For Registration

Utlization by students of Host Institute https://bit.ly/3GZc4e8 **Utilization by students of other Institute** https://bit.ly/35c9ZgU Utilization by faculty members of Host institute https://bit.ly/3G1mDfv Utilization by faculty members of other institute https://bit.ly/3nYoewp Utilization by alumni of Host Institute https://bit.ly/3IrOTcS **Utilization by School Learners** https://bit.ly/33PgGVY **Utilization by School Teachers** https://bit.ly/3H2zS0H **Utilization by Entrepreneur/ start-up** https://bit.ly/3rLkoYx **Utilization by Industry** https://bit.ly/3H36reZ **Event conducted by AICTE-IDEA Lab** https://bit.ly/3r0IsaY Patent registered or granted by Utilization of https://bit.ly/35r91hb **AICTE-IDEA Lab** https://bit.ly/35r91hb Award received by Utilization of AICTE-IDEA Lab, https://bit.ly/3rME7XS Projects completed by using AICTE-IDEA Lab https://bit.ly/3AwJ0s1 Operational Hours of the AICTE-IDEA Lab (Every Day) https://bit.ly/32vMX3N

Investment in AICTE-IDEA Lab



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